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PATENT

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In re Application of:

Yasuyo SOGAWA, *et al.*

Group Art Unit:

Filed: January 18, 2006

For: SEMICONDUCTOR INTEGRATED CIRCUIT

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Customer Window
U.S. Patent & Trademark Office
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

Further to the Information Disclosure Statement filed on April 18, 2006 wherein
Japanese Patent Application No. 52-67741 was cited, filed herewith is a statement provided by
applicants' Japanese patent attorney.

Respectfully Submitted,

STEPTOE & JOHNSON LLP



Roger W. Parkhurst
Registration No. 25,177

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RWP/lmw

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STEPTOE & JOHNSON LLP
1330 Connecticut Ave., N.W.
Washington, D.C. 20036
(202) 429-6420